

Form PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Atty. Docket No. PHA 23,888	Serial No.
	Applicant GEORGE S. FLEMING ET AL			
	Filing Date CONCURRENTLY	Group 2143		

INFORMATION DISCLOSURE CITATION  
 (Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

Ex. Int.	Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
AA	5 3 6 5 5 9 1	11/15/94	CARSWELL ET AL	380	49	
AB	4 9 8 7 5 9 5	1/22/91	MARINO, JR. ET AL	380	50	
AC						
AD						
AE						
AF						

## FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub-class	Trans.	
						Yes	No
AG	0 8 9 6 4 5 1 A 2	2/10/99	EUROPE	H05L	9/06	X	
AH	0 8 7 6 0 2 6 A 2	11/4/98	EUROPE	H04L	9/00	X	
AI							
AJ							
AK							

## OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

AL	"Parallel VLSI Architectures for Cryptographic Systems", by A. DeGloria et al, Proceedings Great Lakes Symposium on VLSI, 1997, (abstract only)
AM	"Case Studies, RISC and Crypto Controllers in the ASIC Bed, by H. Rosocha, (abstract only)
AN	

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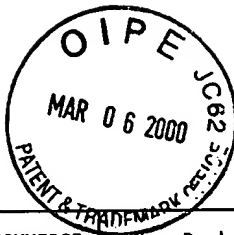
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PATENT AND TRADEMARK OFFICE

Atty. Docket No.

PHA 23,888

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Ex. Int.		Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
	AA	4 1 2 8 8 7 8	12/5/78	YASUHARA ET AL	364	200	
	AB	4 3 9 9 5 0 7	8/16/83	COSGROVE ET AL	364	200	
	AC	5 1 1 7 4 9 8	5/26/92	MILLER ET AL	395	775	
	AD	5 0 5 3 9 5 2	10/1/91	KOOPMAN JR ET AL	364	200	
	AE						
	AF						

**FOREIGN PATENT DOCUMENTS**

		Document Number	Date	Country	Class	Sub-class	Trans.
							Yes No
	AG	0 9 0 8 8 1 3	4/14/99	EUROPE	G06F	9/42	X
	AH						
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**U.S. PATENT DOCUMENTS**

Ex. Int	Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
AA	5 1 6 1 2 4 7	07/1989	MURAKAMI ET AL	395	800	
AB						
AC						
AD						
AE						
AF						

**FOREIGN PATENT DOCUMENTS**

Document Number	Date	Country	Class	Sub-class	Trans.
					Yes No
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